

Exam Regulations for EE-320 (Analog IC design) – Fall 2024

The duration of the exam is 3 hours. Students with special arrangements will complete the exam according to their approved terms.

You can bring one page with formulas written on one or both sides of it (handwritten, or typed and printed out). Printed slides of the course, exercises, homework, books, or other materials are not allowed.

Sheets of paper with space allocated to each answer will be provided. The blank sides on the reverse of each page may also be used for calculations. All writings must be on these sheets and the final answers to each exercise must be clearly written in the space provided for this purpose.

You need to show all your developments (with diagrams and calculations).

A normal or scientific calculator is allowed. Please bring your calculator with you. No calculator will be provided during the exam.

Laptops, cell phones, tablets, or any other equivalent electronic device are prohibited during the exam.

Each student will be placed by us in the examination room which means that they will not be able to sit where they want.

Each student will use a pen, not a pencil, to solve the exercises.

Teachers will not answer any questions during the exam unless there is an error in the wording of an exercise or a problem with understanding the wording of an exercise.

In accordance with EPFL rules, any attempt by a student to cheat will result in the cancellation of their exam which will receive a zero mark on the exam.