

Weekly Schedule (may be updated as needed)

Week	Subject by week – EE-320: Analog IC design – Fall 2025	Suggested Chapters
Week 1: 08/09 – 14/09	Introduction, organization, review of MOS transistors + Exercise1	Ch 1, Ch 2.1-2.4, Slides on Moodle
Week 2: 15/09 – 21/09	MOS large and small-signal models, regimes of operations + Exercise2	Ch 2.1-2.4
Week 3: 22/09 – 28/09	Holiday – No class	
Week 4: 29/09 – 05/10	MOS parasitic effects, layout basic, single-stage amplifiers + Exercise3	Ch 2.1-2.4, Ch 3.1
Week 5: 06/10 – 12/10	Single-stage amplifiers + Exercise4	Ch 3.1-3.7
Week 6: 13/10 – 19/10	Single-stage amplifiers + Exercise5	Ch 3.1-3.7
Week 7: 20/10 – 26/10	Break – No class	
Week 8: 27/10 – 02/11	Single-stage amplifiers + Cascode + Exercise6 + Homework1	Ch 3.1-3.7
Week 9: 03/11 – 09/11	Differential amplifiers + Exercise7	Ch 4.1-4.4
Week 10: 10/11 – 16/11	TP1 Practical exercise session on Cadence	Tutorial on Moodle
Week 11: 17/11 – 23/11	TP2 Practical exercise session on Cadence	Tutorial on Moodle
Week 12: 24/11 – 30/12	TP3 Practical exercise session on Cadence + Homework2	Tutorial on Moodle
Week 13: 01/12 – 07/12	TP4 Practical exercise session on Cadence	Tutorial on Moodle
Week 14: 08/12 – 14/12	Differential amplifiers, current mirrors + Exercise8	Ch 4.1-4.4, Ch 5.1-5.3
Week 15: 15/12 – 22/12	Current mirrors + Exercise9	Ch 5.1-5.3

Assessment:

- **Written final exam:** 70% of the final grade
- **Homework (2 in total):** 30% of the final grade