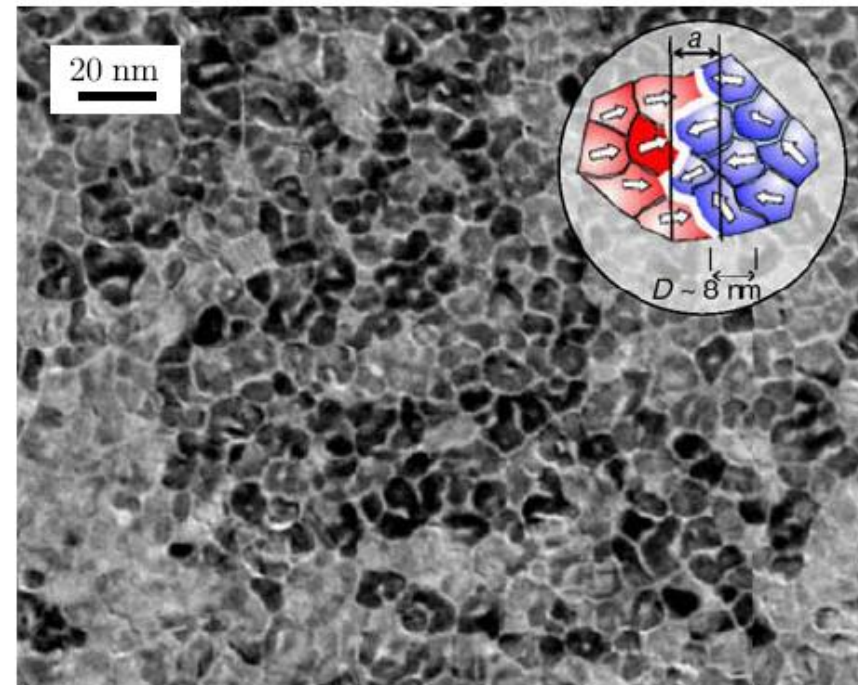


Some basic questions

... that need an answer



CoCrPt recording layer

- 1) Why every grain has a magnetic moment?
- 2) How the material is chosen?
- 3) Why every grain is magnetically decoupled from the neighbors
- 4) Why the grain magnetization is pointing in one specific direction?
- 5) Why the magnetization direction changes from grain to grain?
- 6) Why the grain magnetization does not fluctuate with time?
- 7) ...

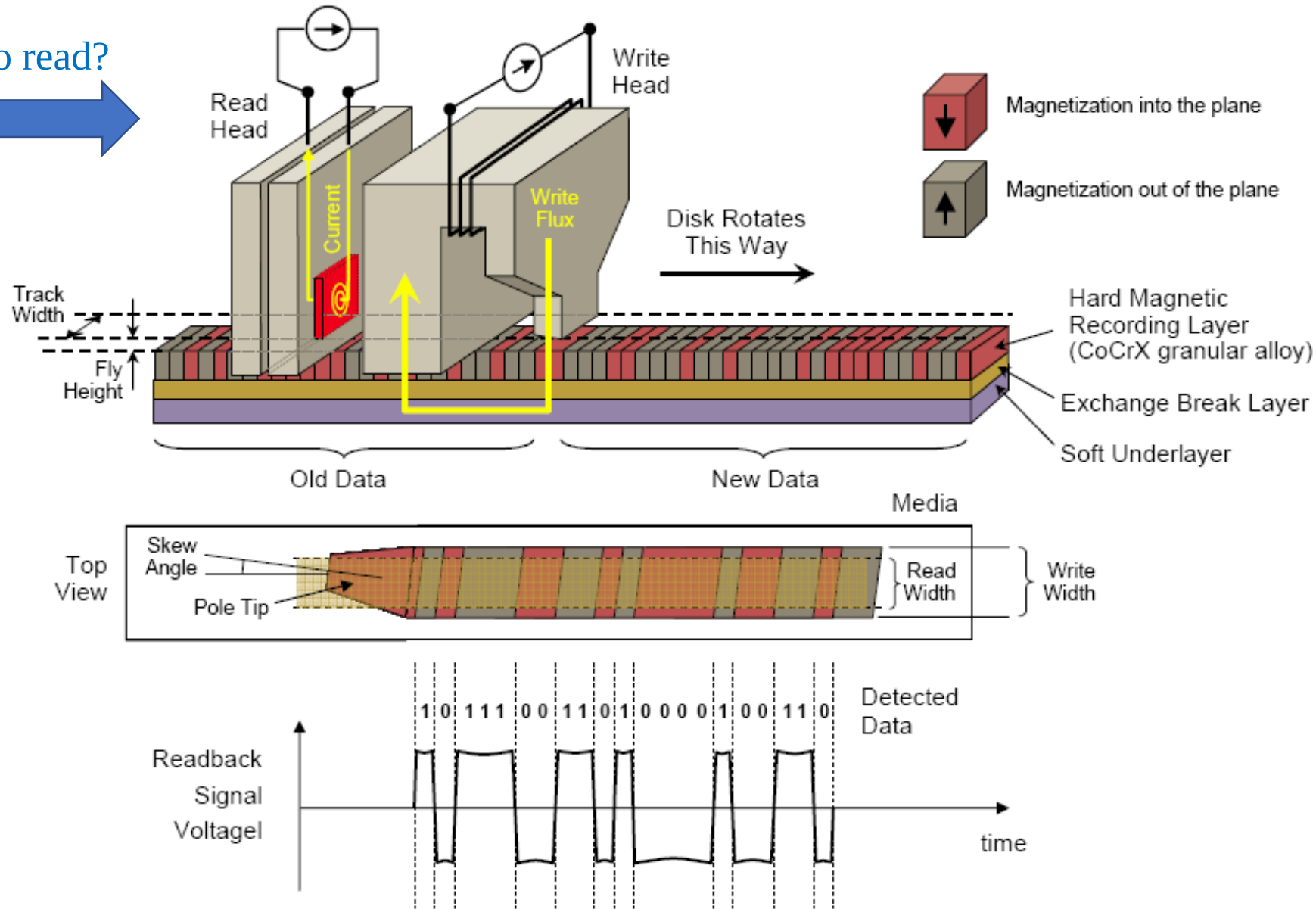
Atomic magnetism:
- Spin and orbital

Exchange energy

Magnetic anisotropy:
- Crystalline
- Shape

The recording mechanism: a condensate of concepts

How to read?



Information processing: electronics vs spintronics

1) Information storage

HDD: hard disk drive

High density magnetic storage



SSD: solid state drive

Solid state non volatile memory



2) Fast access memory: temporary work space in your device

Dynamic random-access memory (DRAM): random-access memory that stores each bit of data in separate capacitors within an integrated circuit
(=> Single electron transistor to minimize the size of the capacitor)

Since the capacitors used in DRAM lose their charge over time, **DRAM must refresh** approximately 20 times a second



Volatile



Spintronics: combining the electron charge and spin

Magnetic random access memory (MRAM) never requires a refresh.
The memory will keep the information also with the power turned off

much **lower power consumption** (up to 99% less) compared to DRAM

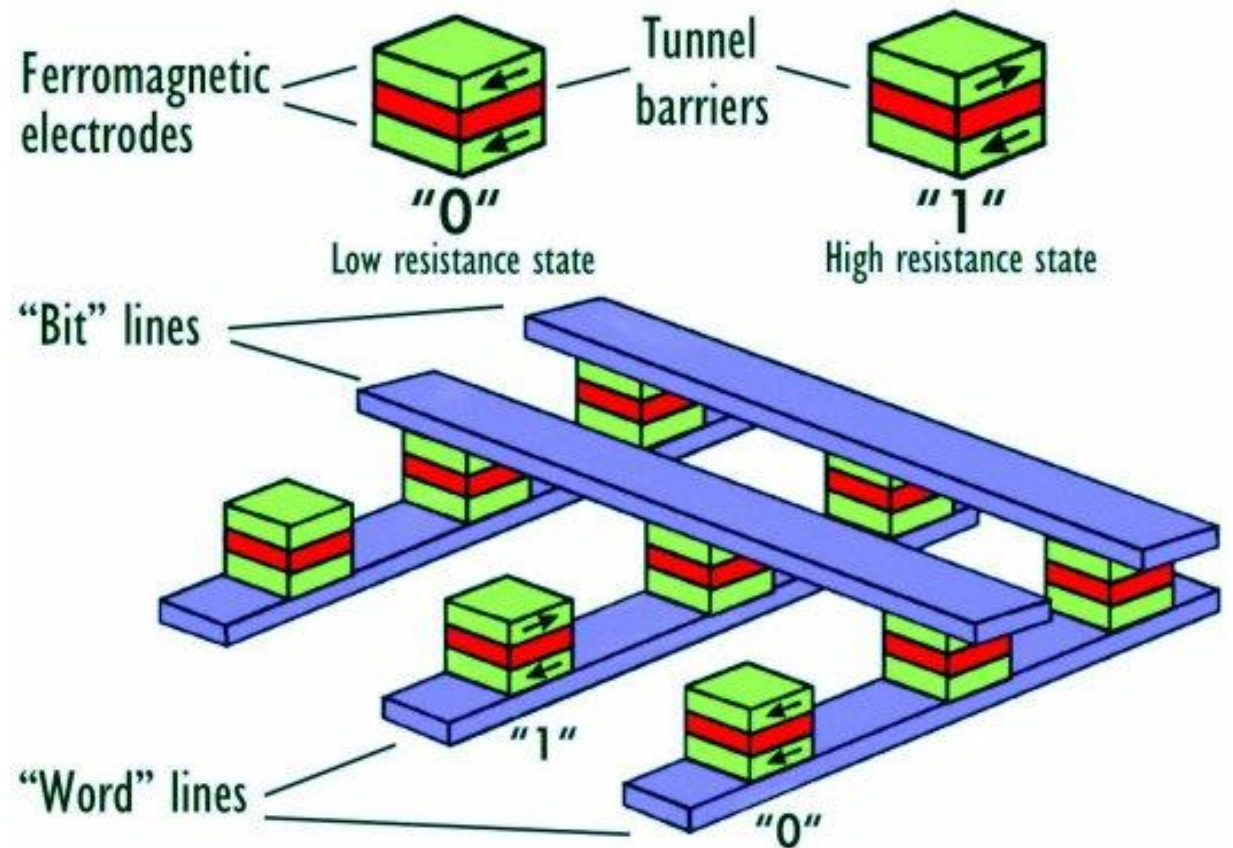
However, with storage density and capacity orders of magnitude smaller than an hard disk (HDD), **MRAM is useful in applications where moderate amounts of storage with a need for very frequent updates are required**

In production



<https://www.everspin.com/>

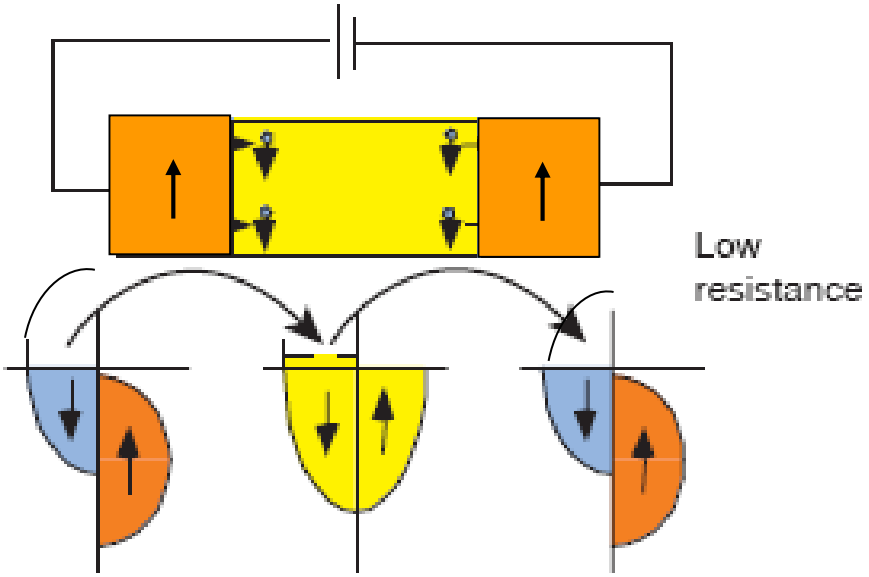
Ferromagnetic (FM) - nonmagnetic (NM) - ferromagnetic (FM) junction



Science **282**, 1660 (1998); Nat. mater. **6**, 813 (2007); doi.org/10.1016/j.mattod.2017.07.007

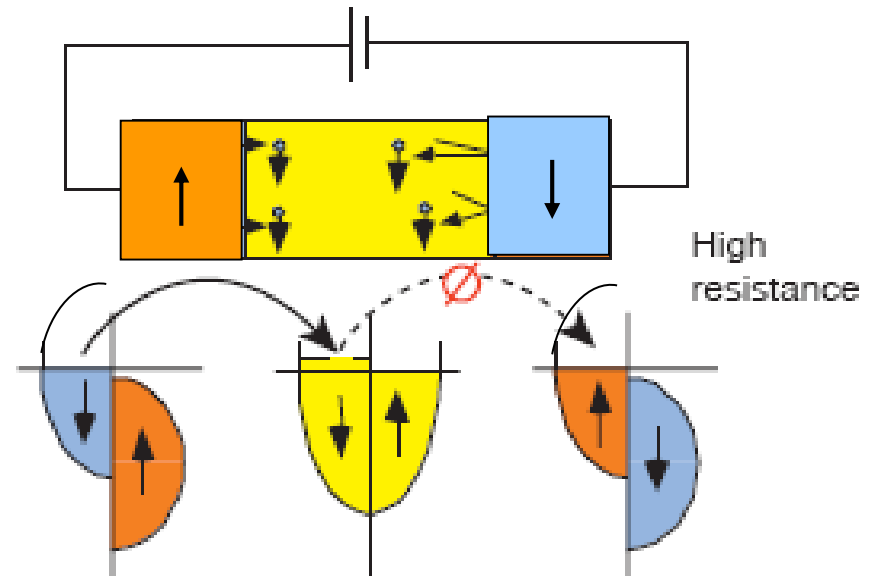
Spin transport

1



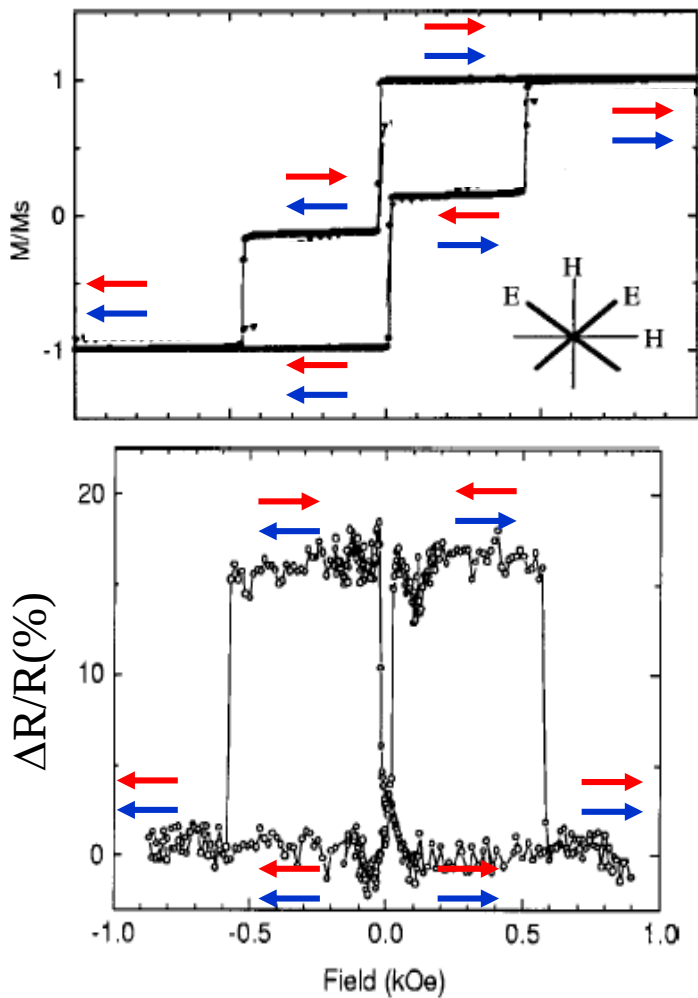
Available free states with the same spin:
Low resistance

0

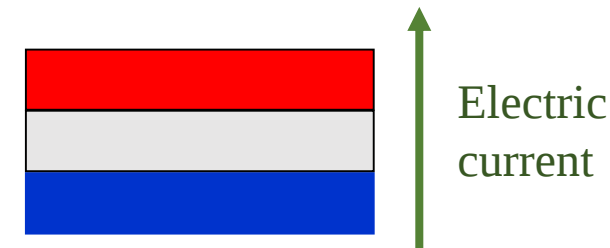


Absence of free states with the same spin:
High resistance

Magnetoresistance



Pinned FM $\rightarrow$
metallic or insulating NM spacer
Free FM $\rightarrow$



Pinned layer:

layer with high reversal field (i.e. high MAE)

Free layer:

layer with low reversal field (i.e. low MAE)

$$\Delta R/R = \frac{R_{\uparrow\downarrow} - R_{\uparrow\uparrow}}{R_{\uparrow\uparrow}}$$

High (low) resistance for anti-parallel (parallel) alignment of the magnetization in the two ferromagnetic layers

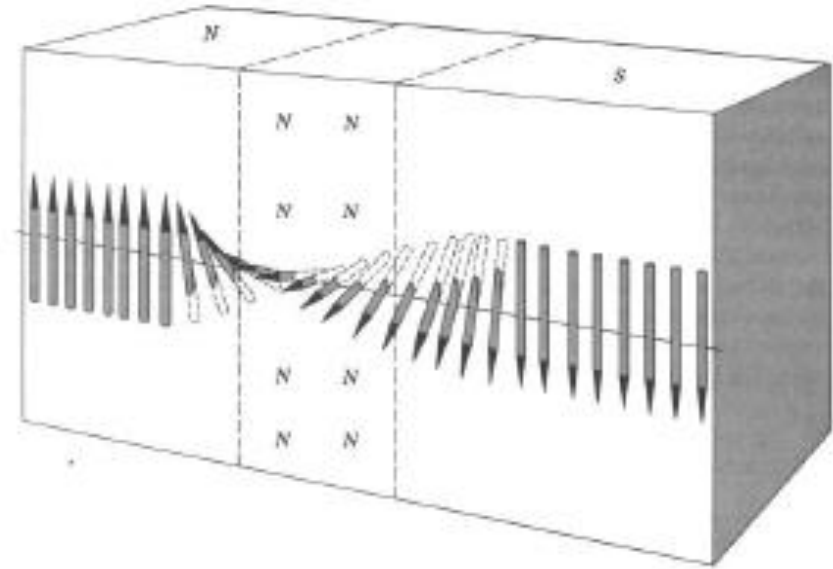
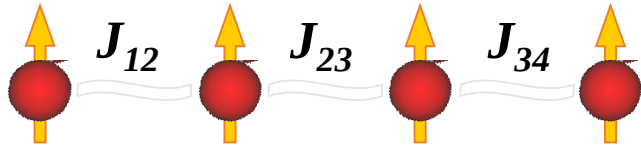
Role of the non magnetic spacer

The atom spins are coupled together by
inter-atomic exchange

Inter-atomic exchange:

MAGNETIC ORDER

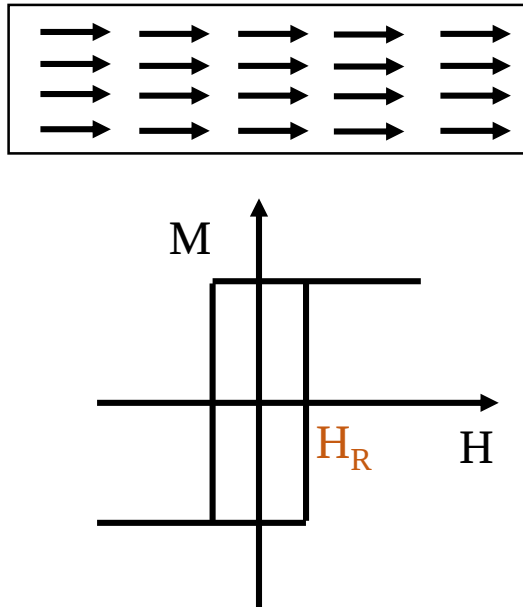
$$H_{exc} = - \sum_{i \neq j} J_{ij} \mathbf{S}_i \cdot \mathbf{S}_j$$



In non magnetic materials $S = 0$ thus $H_{exc} = 0$ i.e. decoupling of
free and pinned layer

How to make the pinned layer: exchange bias

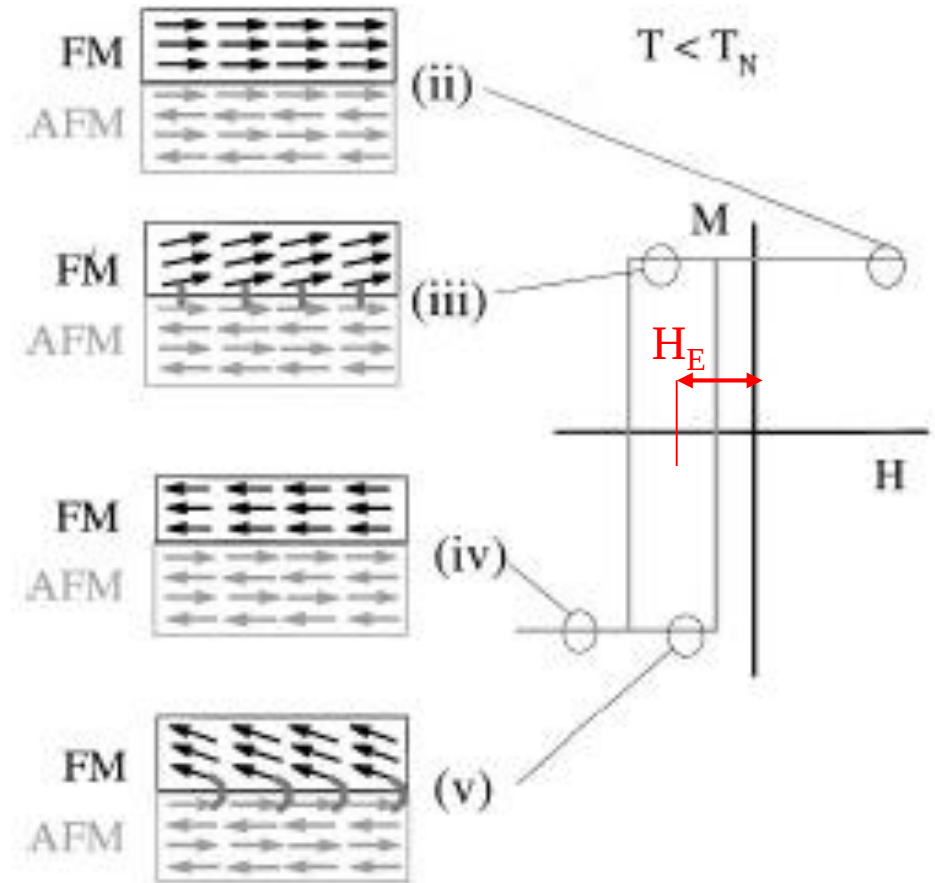
FM: ferromagnetic material; AFM: antiferromagnetic material



In a FM layer the magnetization reverses at a field H_R (reversal or coercive field)

H_E : exchange bias field is the magnetic field shift of the hysteresis curve

Due to the exchange interaction at the FM-AFM interface, the field necessary to orient the FM layer parallel to the direction of the surface AFM spins is reduced compared to the field necessary to force the anti-parallel alignment by an amount equal to H_E

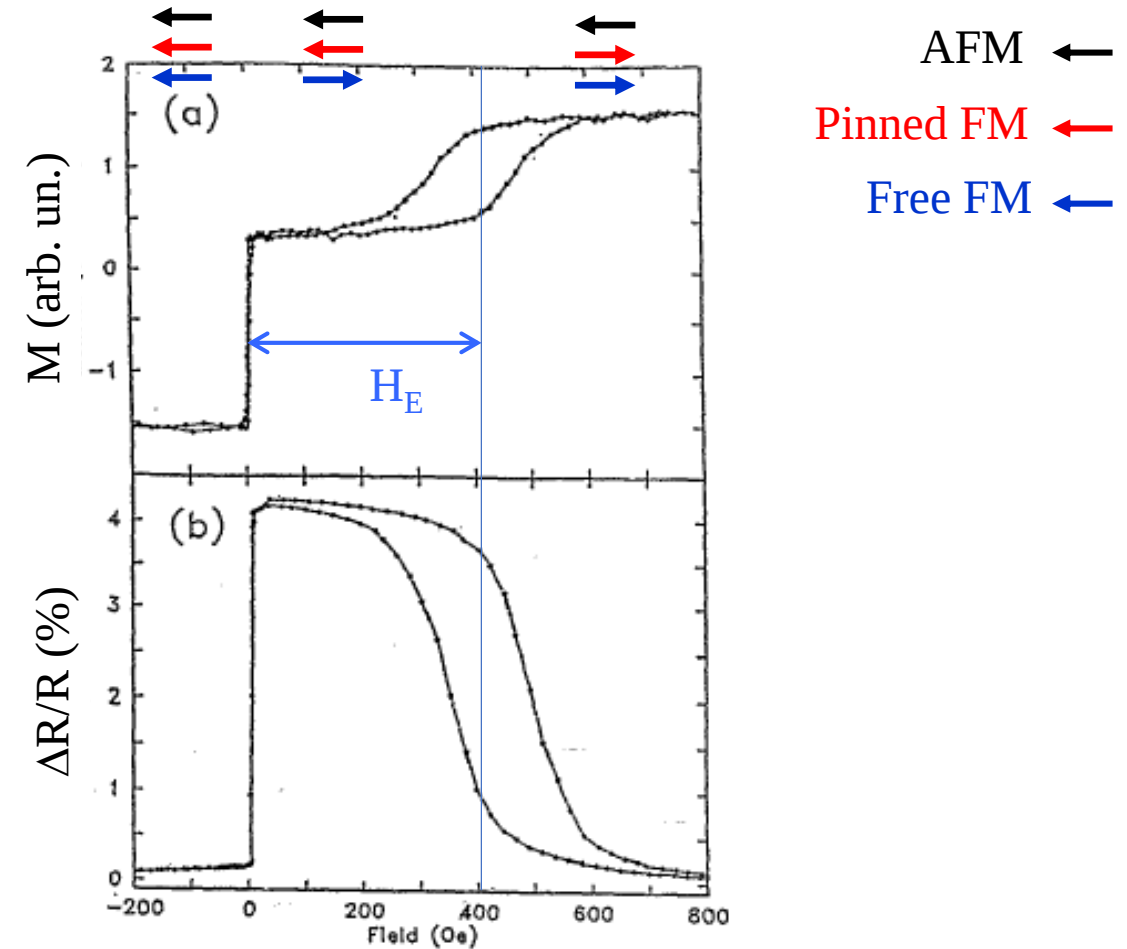
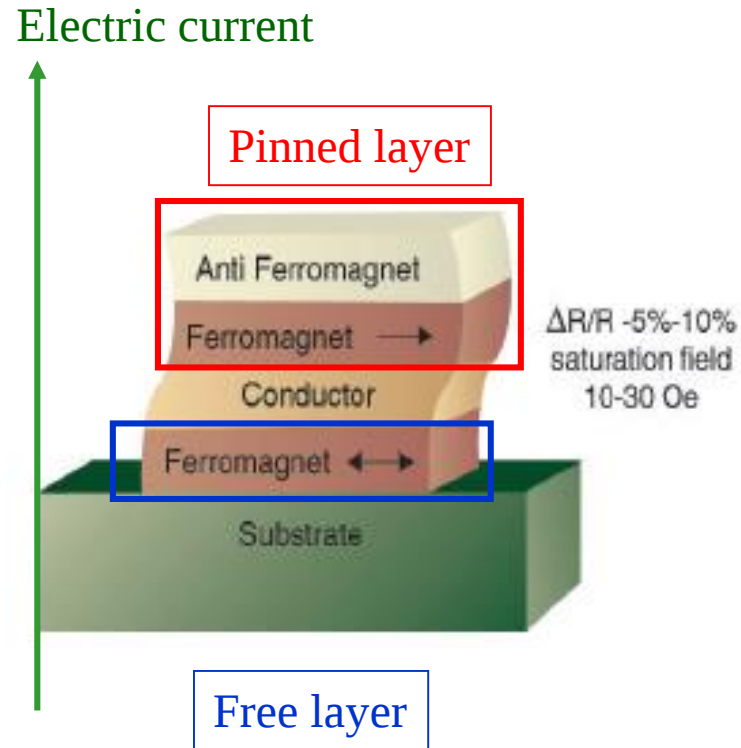


The magnetic field required to reverse the pinned layer becomes

$$H_R + H_E$$

GMR: giant magneto resistance

The spacer is a metal

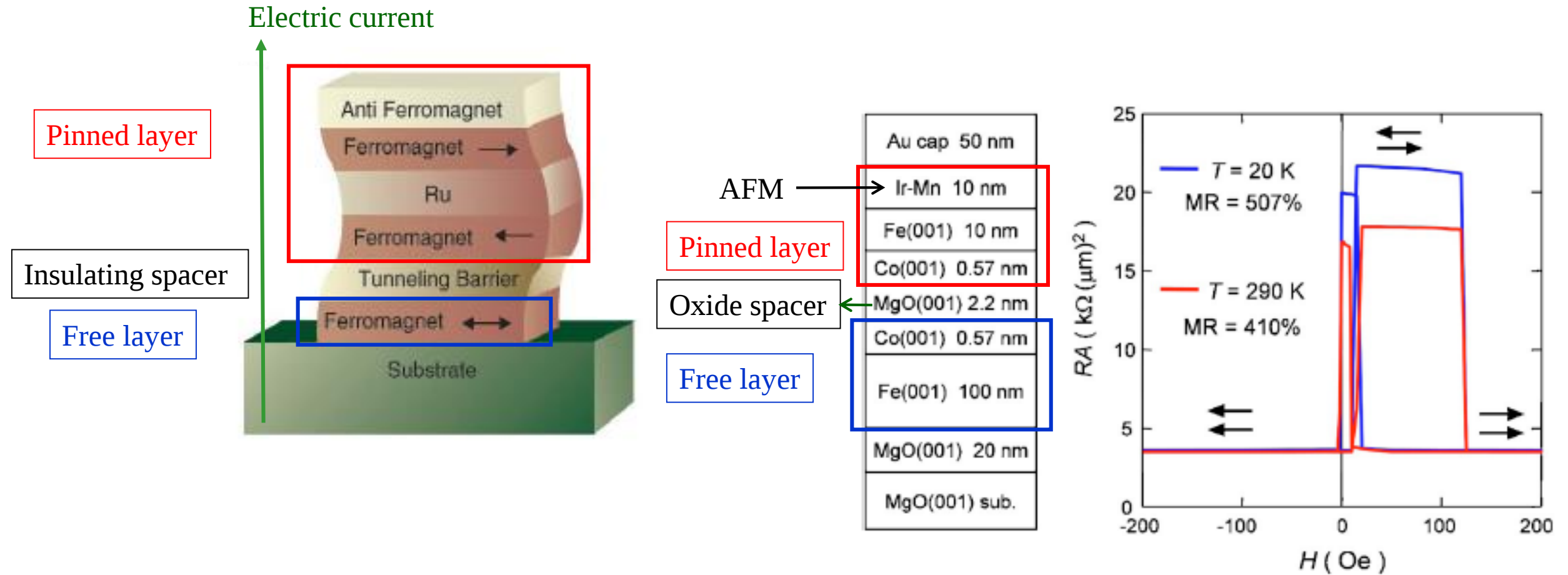


The electric resistance depends on the respective spin orientation of the two FM layers:

Low $\rightarrow$ parallel alignment

High $\rightarrow$ anti-parallel alignment

MTJ: magnetic tunnel junction



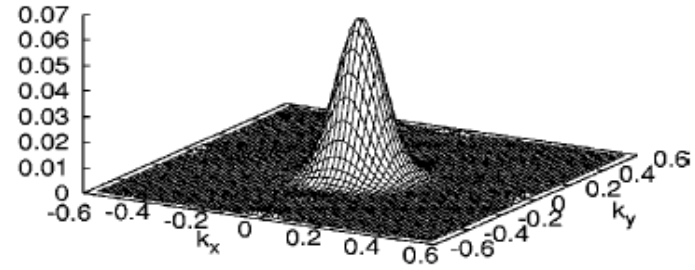
Improved performance with respect to GMR due to:

- 1) precise control of layer thickness
- 2) high control of crystallographic structure and
- 3) optimum choice of the materials

Role of the spacer thickness

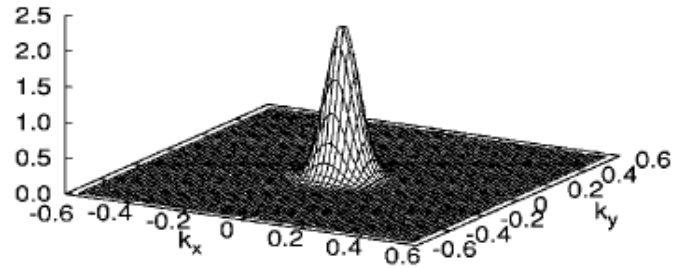
4 MgO Layers Majority

Transmission



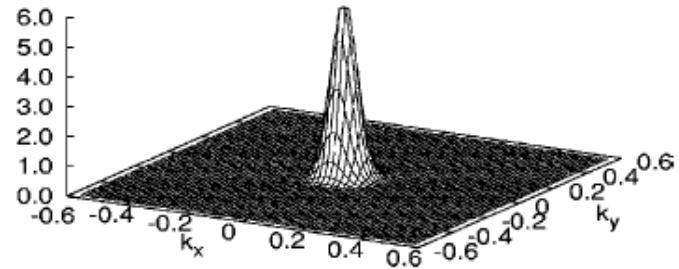
Majority Conductance for 8 MgO Layers

Transmission X 10⁴



Majority Conductance for 12 MgO Layers

Transmission X 10⁷



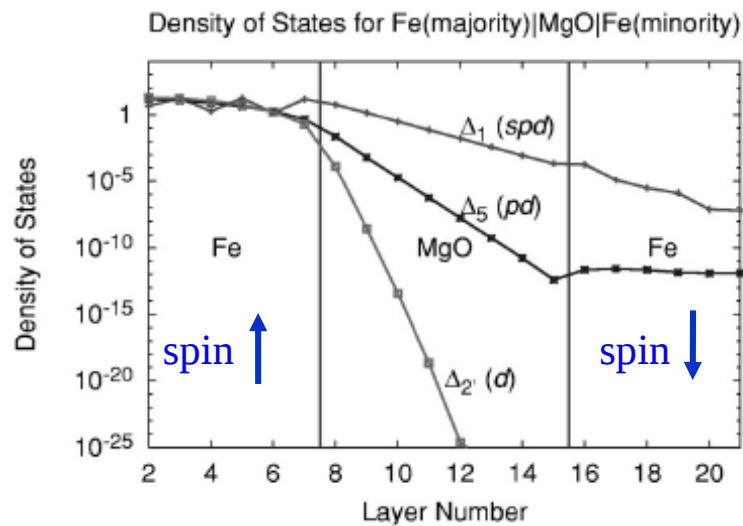
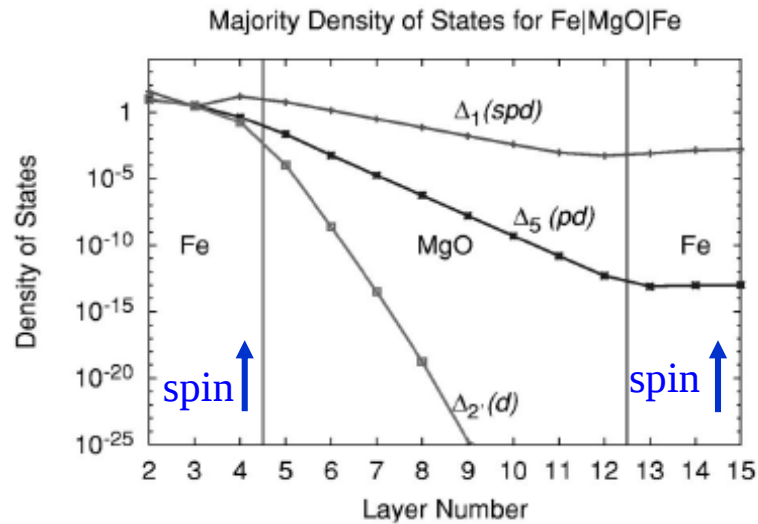
T = transmission through the tunnel barrier

$$T \propto e^{-2d \sqrt{\frac{2m(\Phi - E_F)}{\hbar^2} + k_{\parallel}^2}}$$

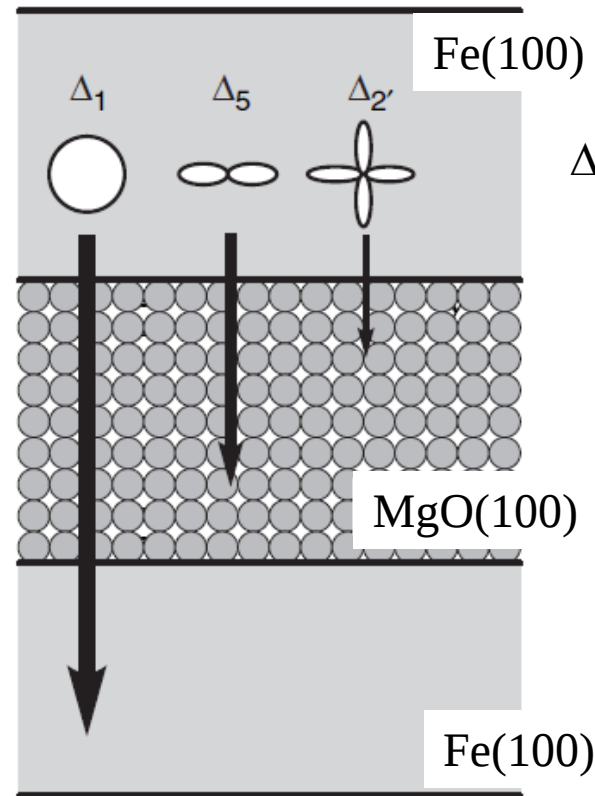
Transmission strongly dependent on MgO thickness d -> high control of MgO roughness to have flat interfaces and thus uniform current

FIG. 6. Majority conductance for 4, 8, and 12 layers of MgO. Units for k_x and k_y are inverse bohr radii.

Role of the spacer material and crystal structure



Electron wave function symmetries

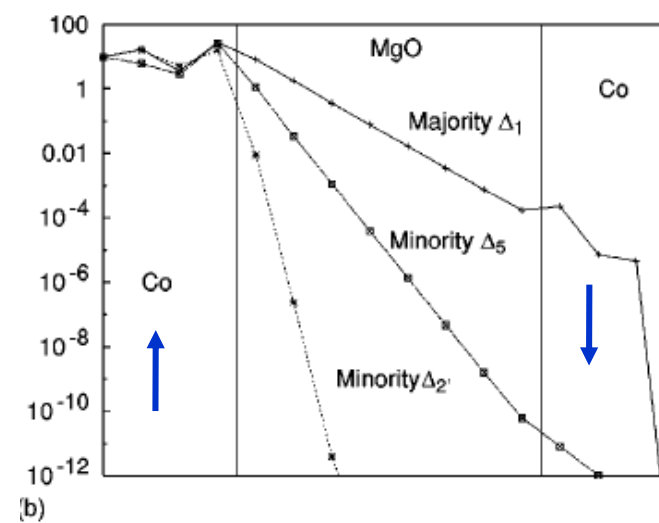
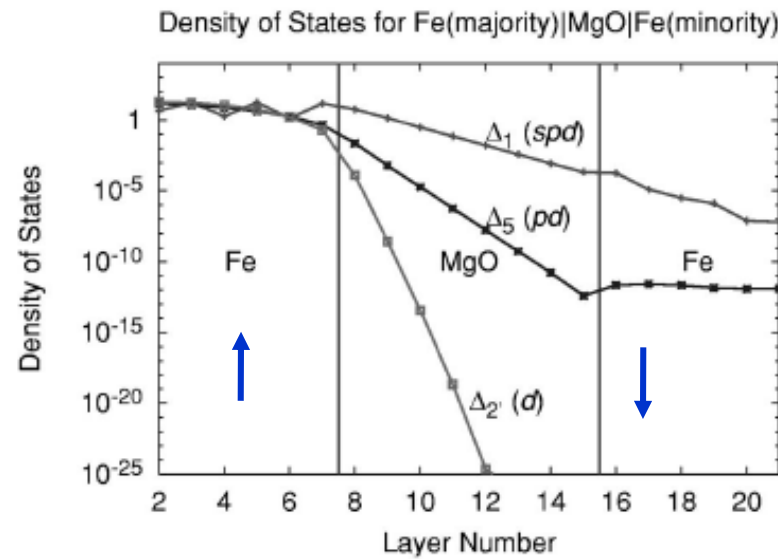
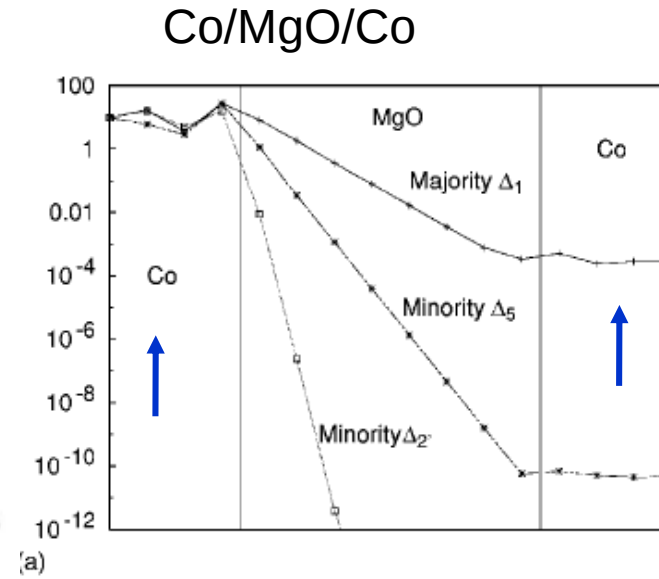
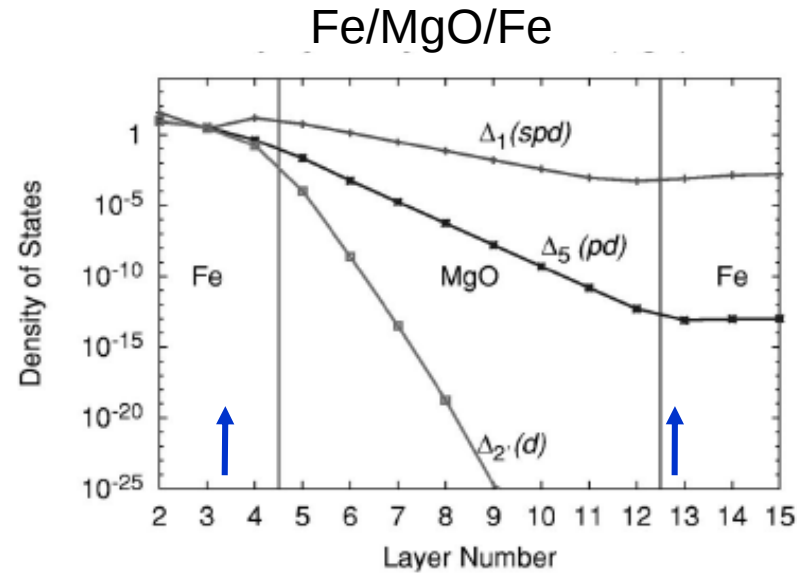


Δ_1 -> totally symmetric wave function with respect to the normal to the tunnel barrier: s, p_z , d_{z^2} .

Δ_1 is a slowly decreasing evanescent state for majority spins.

Transmission depends on the electronic state symmetry, materials and crystal structure
 -> optimization of material crystal structure to optimize the spin junction performance

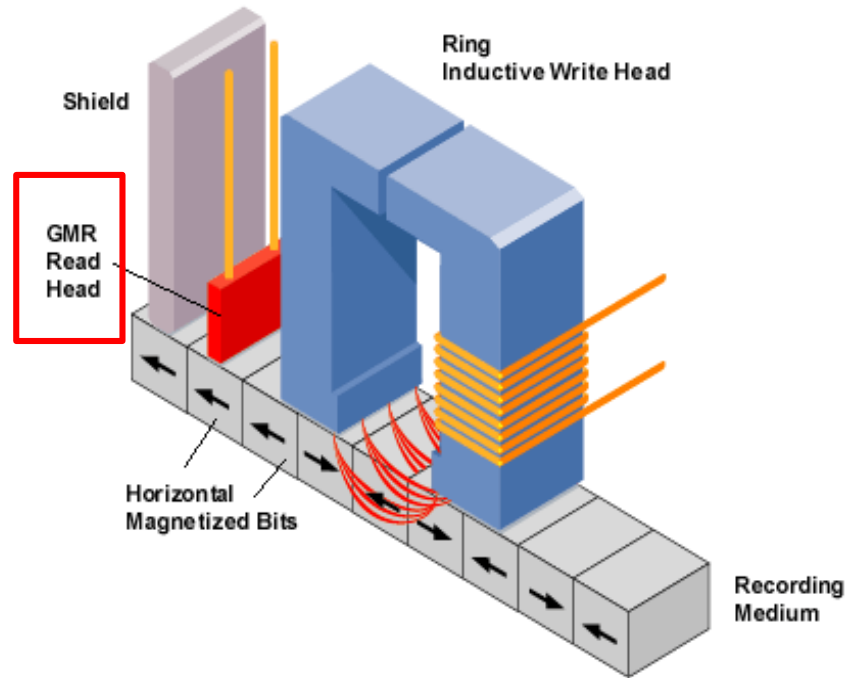
Role of the magnetic layer composition



Total reflection of tunneling electrons
for antiparallel spin alignment in
Co/MgO/Co junction ->
higher TMR in Co/MgO/Co than in
Fe/MgO/Fe junction

The electronic DOS of the two electrodes determines the TMR

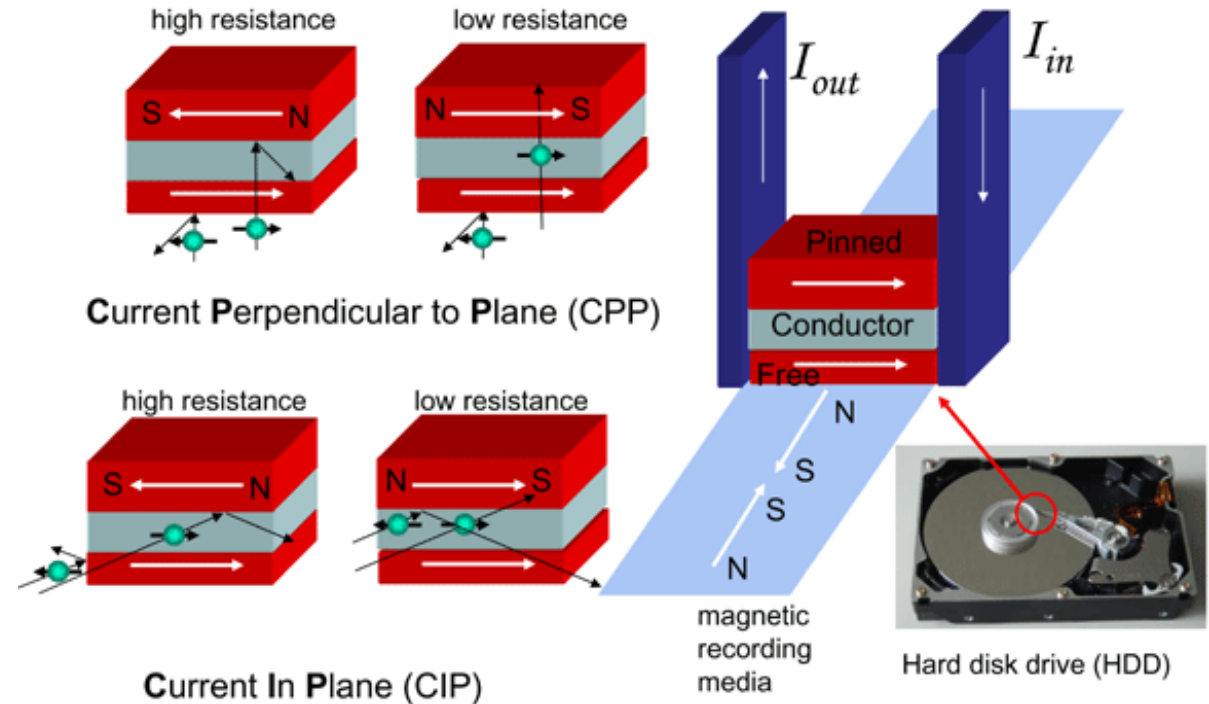
Reading-writing head in HDD



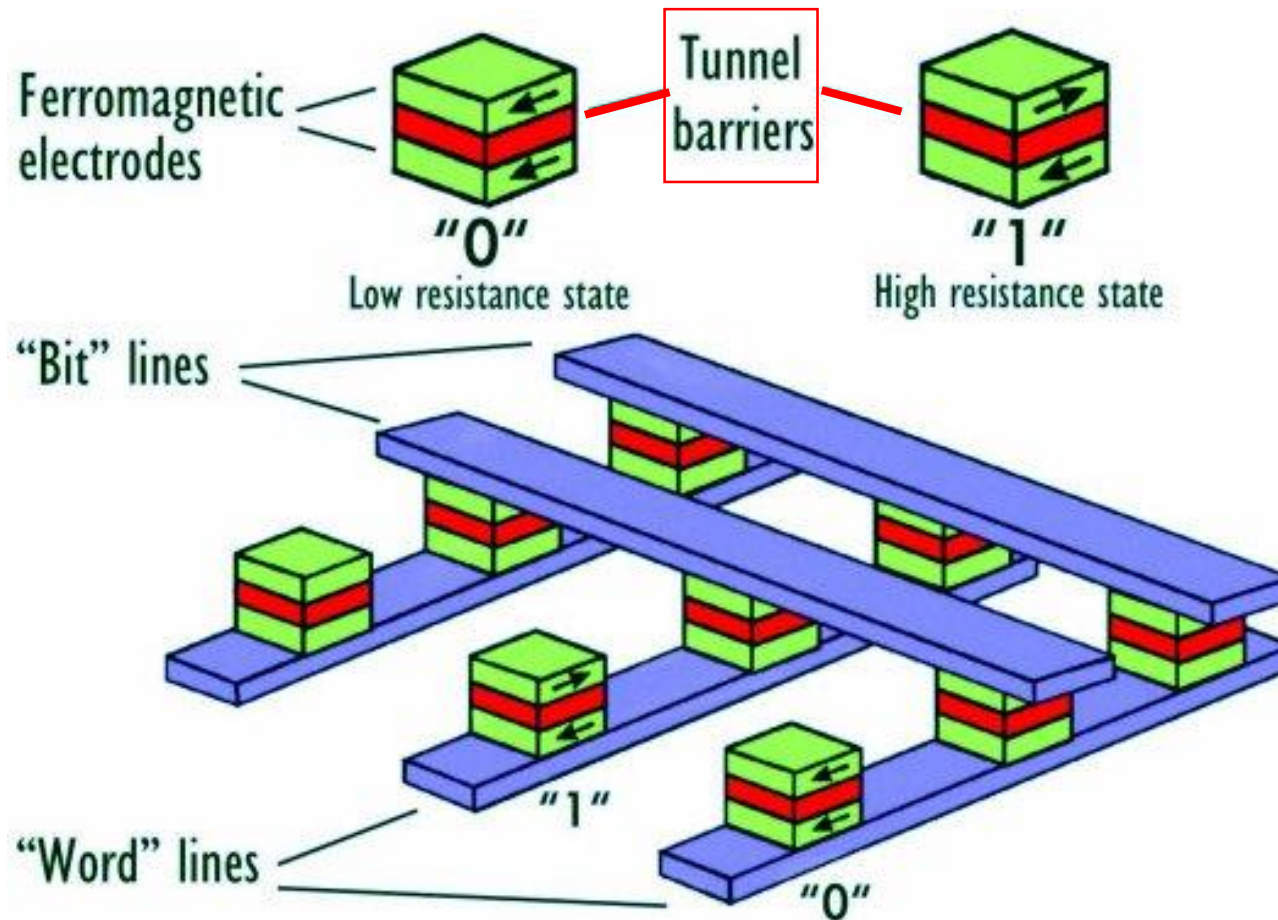
Writing:
the head stray field defines the magnetization
direction of the recording medium

Reading:
the bit stray field defines the magnetization
direction of the free layer

Giant Magnetoresistance (GMR)



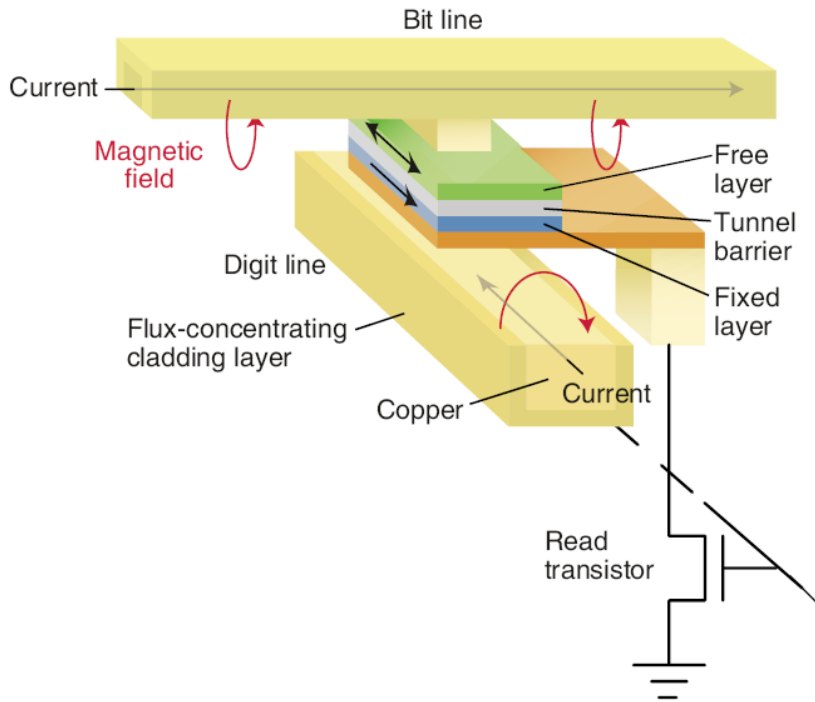
Magnetic random access memory (MRAM)



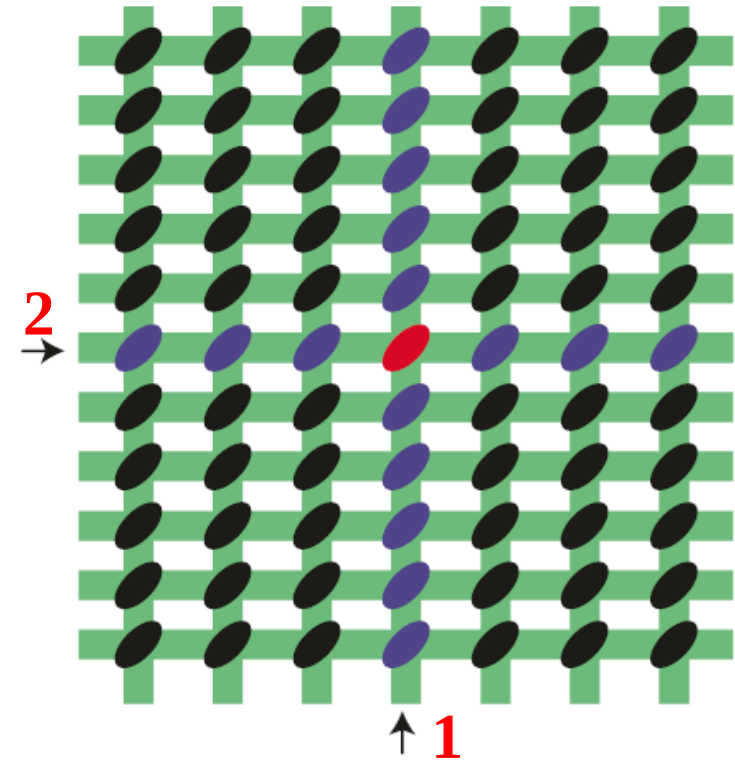
Reading: by measuring the point contact resistance between a bit and a word line

Writing: by induced magnetic fields or by injecting spin polarized current through the point contact

MRAM writing by a magnetic field



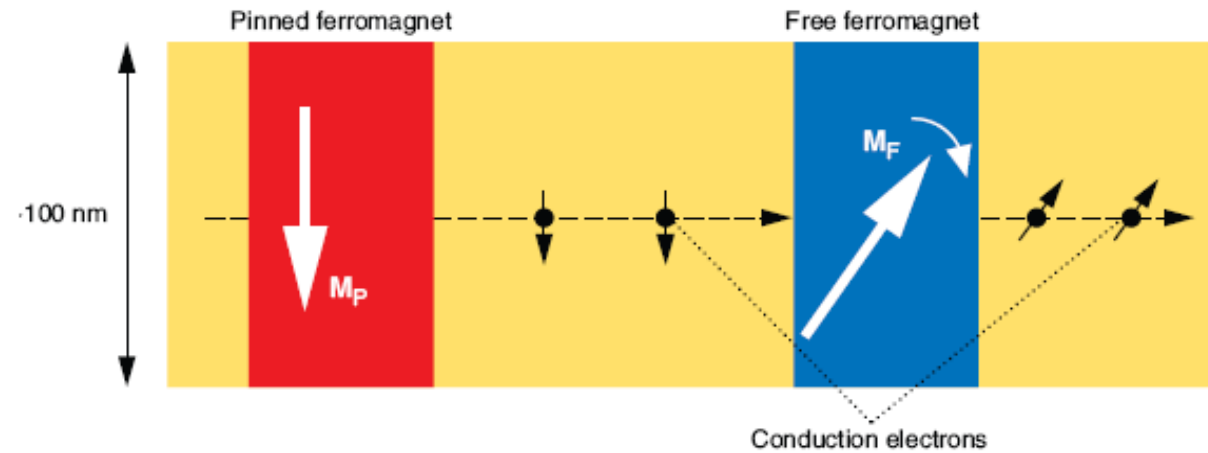
The MRAM is engineered in such a way that the bit easy axis points at 45° to the bit and digit lines



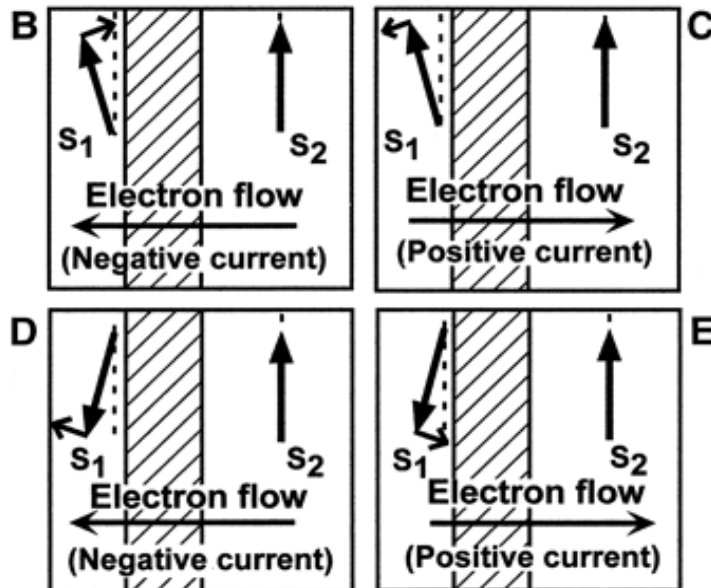
Word line = digit line

Line 1 produces the magnetic field necessary to turn by 45° the bit magnetization
Line 2 produces the magnetic field necessary to complete the reversal of the magnetization of the selected (red) bit
Switching off the magnetic fields generated by bit and digit lines, the magnetization of the non-selected bits relaxes back to the original direction

MRAM writing by a spin polarized current



- The current flowing through the pinned layer gets spin polarized
- The free layer exerts a torque on the spin of electrons flowing through it
- According to the Newton's third law, the electron must exert an equal and opposite torque on the magnet, which causes the magnetization reversal

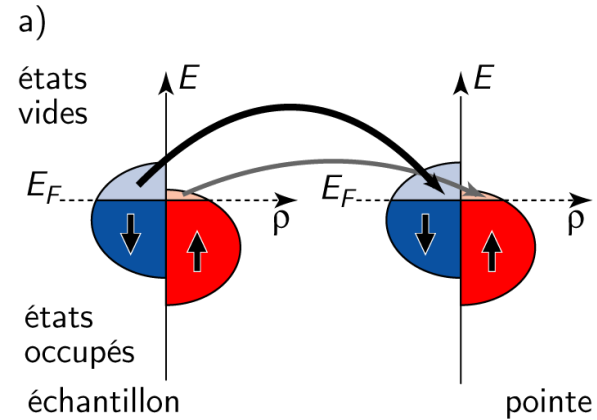
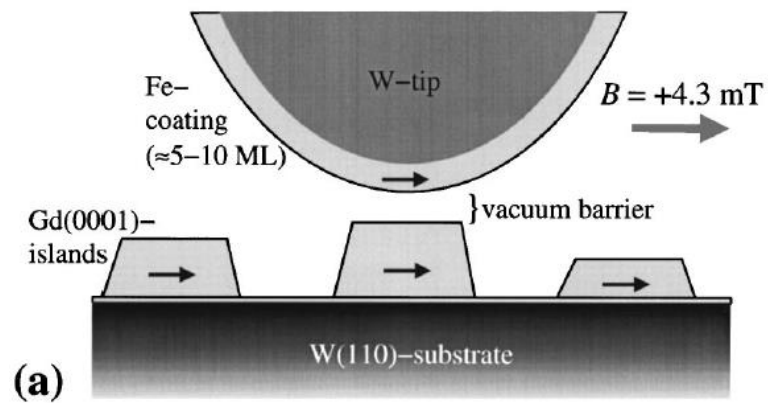


$$\tau_{1,2} \propto I \underline{S}_{1,2} \times (\underline{S}_1 \times \underline{S}_2)$$

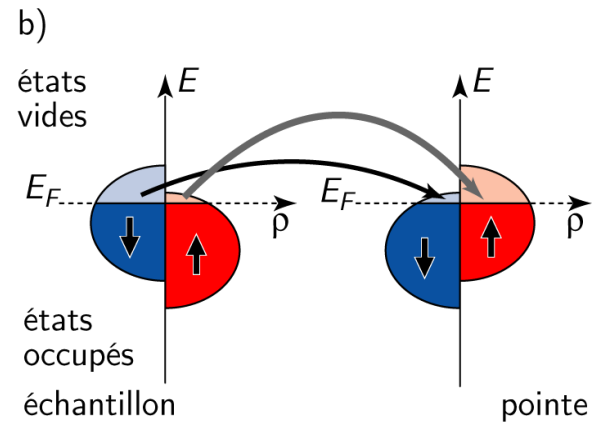
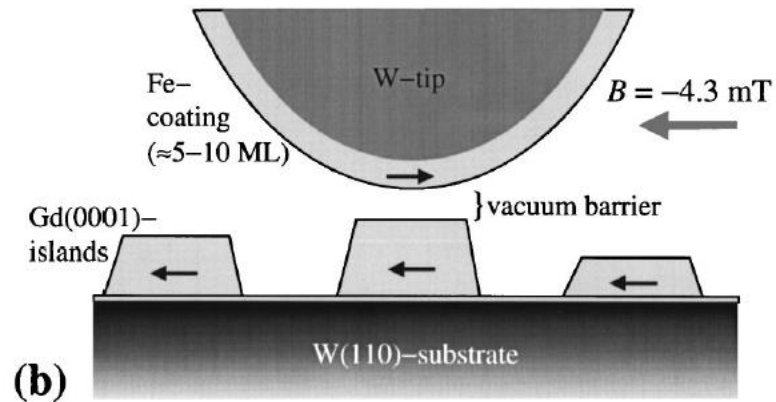
Torque depends on the current direction:
parallel or antiparallel alignment of the magnetization of pinned and free layer can be selected

Spin polarized STM

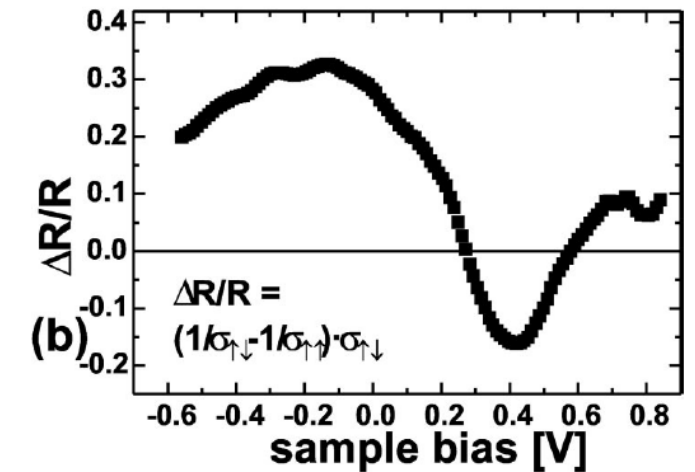
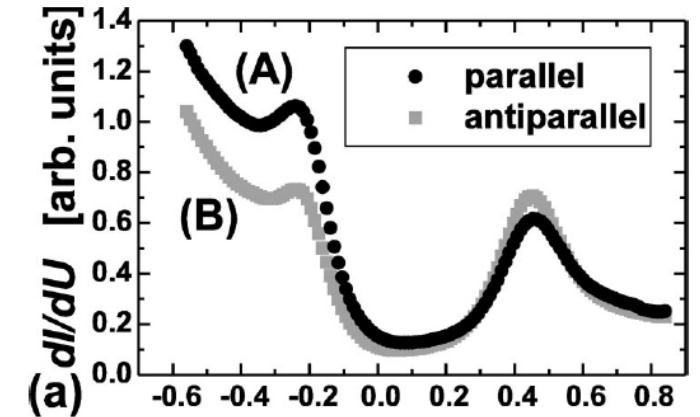
High current



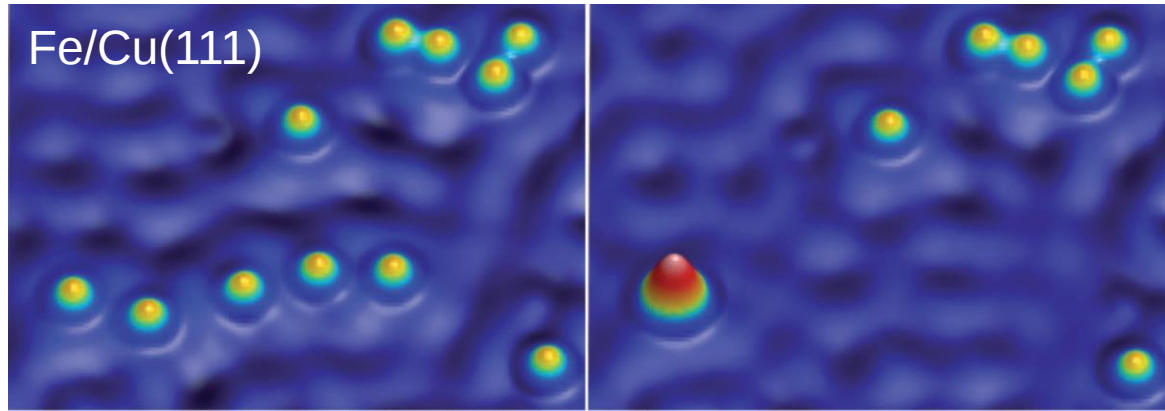
Low current



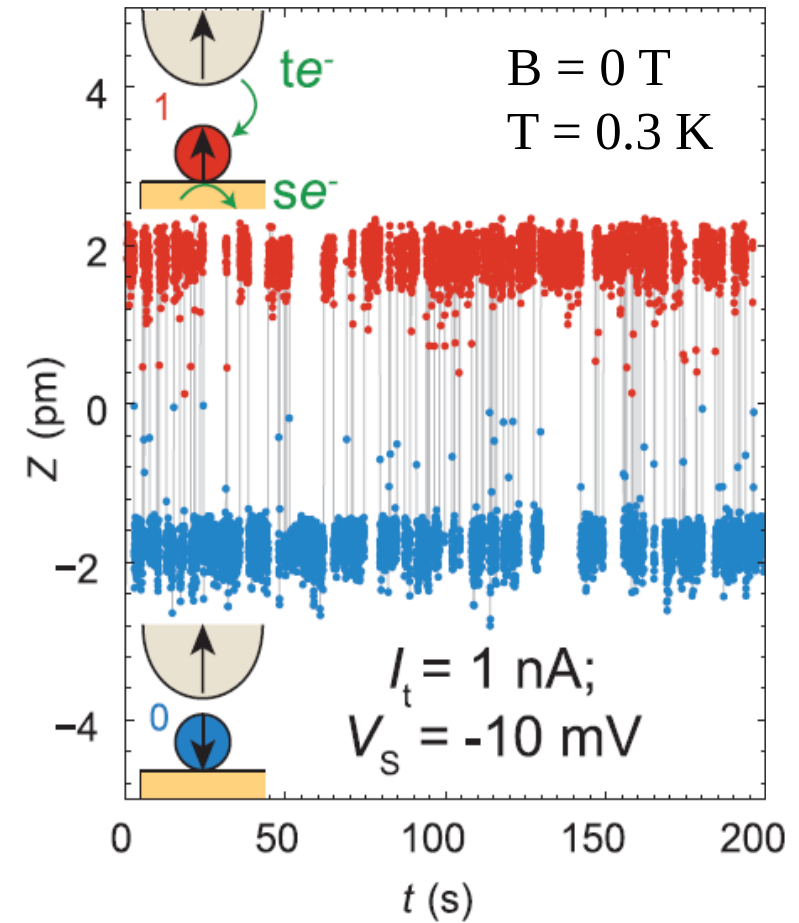
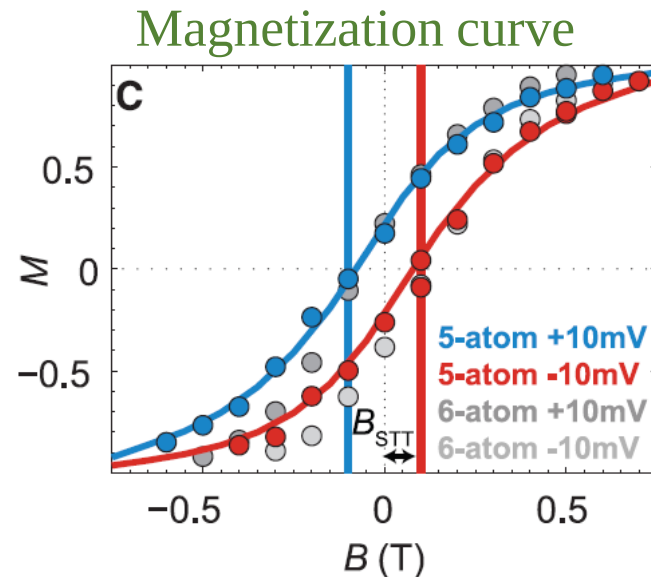
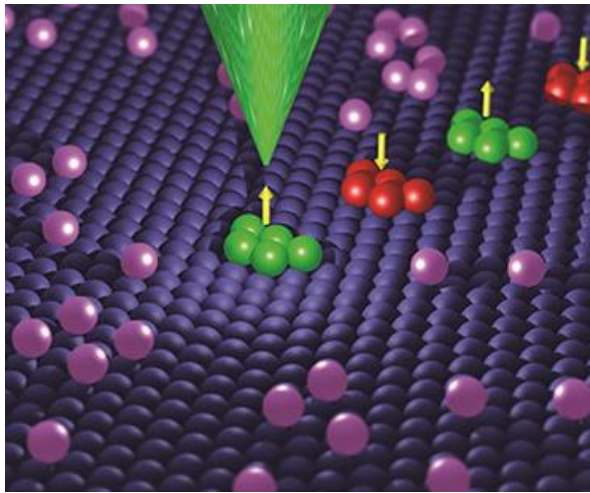
In Practice: the bias voltage defines the optimum sensitivity by selecting the spin polarized states



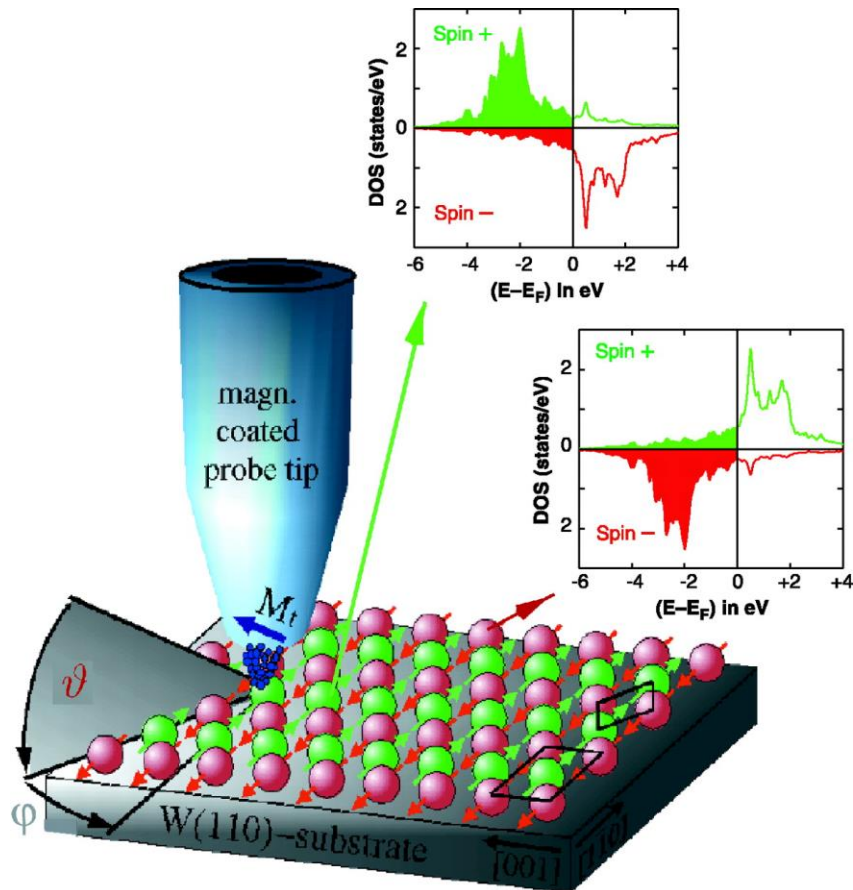
Artificially constructed quantum magnet



Spin life time:
switching between
the two spin states



Real-Space Imaging of Two-Dimensional Antiferromagnetism on the Atomic Scale



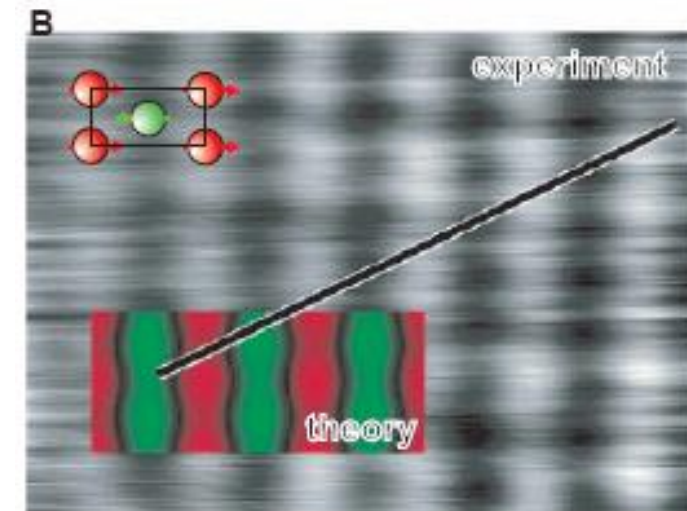
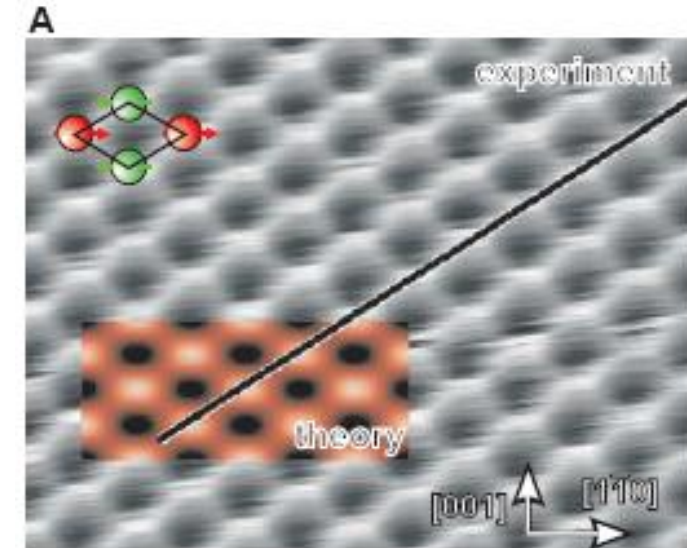
W tip

1x1
unit cell

Fe coated tip

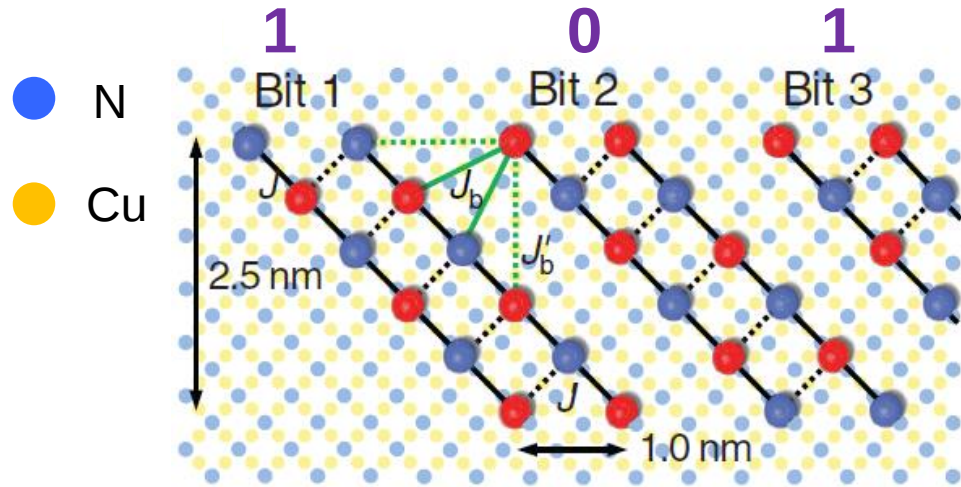
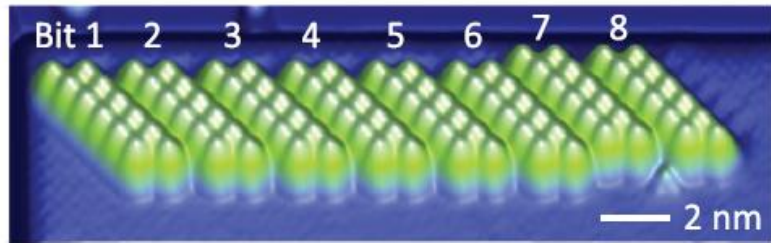
c 2x2
magnetic
unit cell

1 monolayer Mn/W(110)



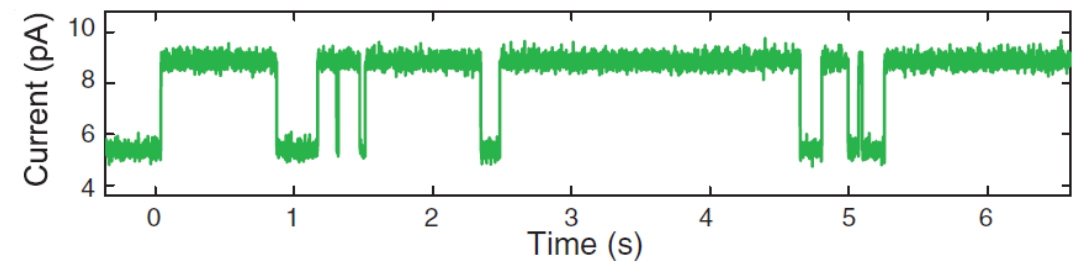
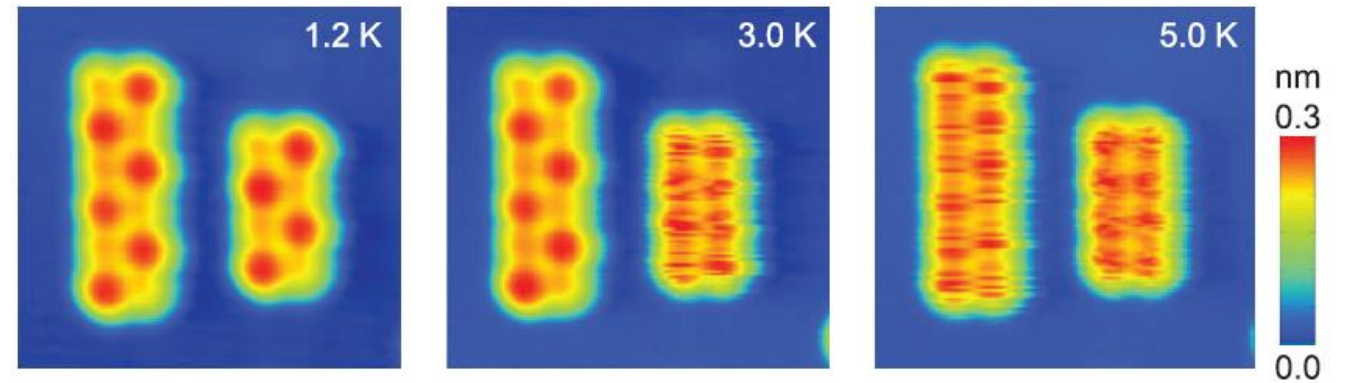
Bistability in Atomic-Scale Antiferromagnets

Fe nanostructures on Cu_2N



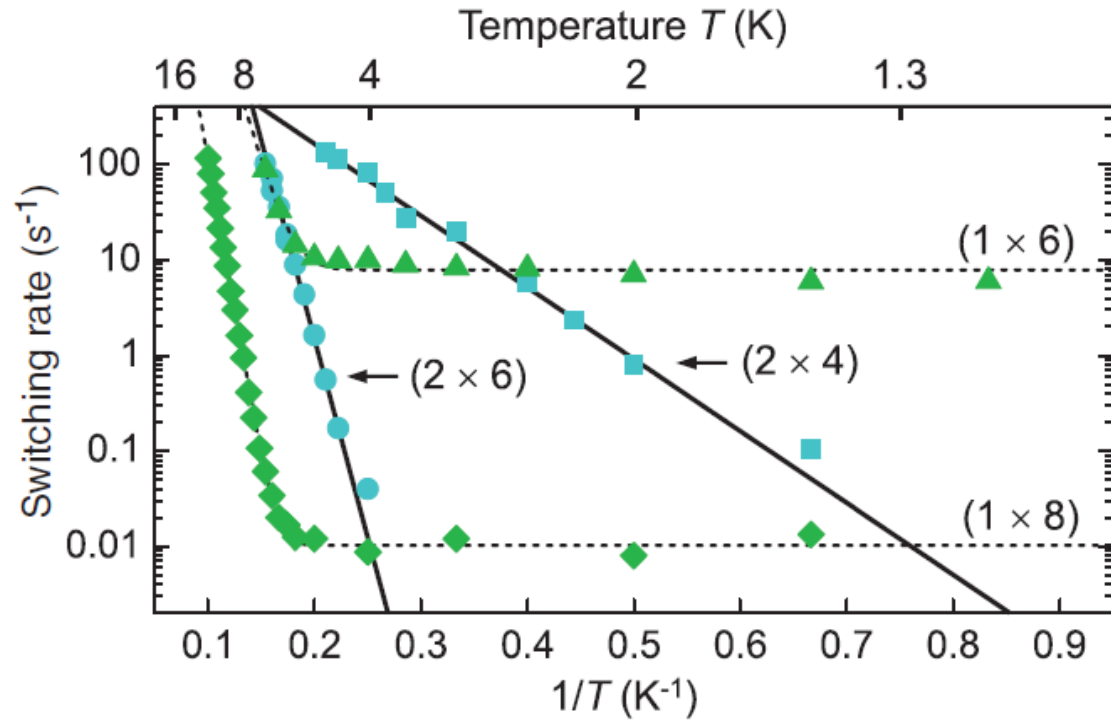
J : exchange coupling between atoms in a bit
 J_b , J'_b : exchange couplings between atoms in neighboring bits.

Temperature stability



Switching between the two Néel states

Size dependent spin-reversal mechanism



Reversal mechanism depends on the bit size and on T

Reversal over the top of the barrier or via excited state (assisted QT) is T dependent: $\tau = \tau_0 \exp(\Delta E/k_B T)$

QT is temperature independent

