Course Schedule & new room for TPs

Week	Subject by week – EE-320: Analog IC design – Fall 2024	Suggested Chapters
Week 1: **09/09** – 15/09	**Introduction, organization, review of BJT and MOS transistors** + Exercise1	Ch 1, Ch 2.1-2.4, Slides on Moodle
Week 2: **16/09** – 22/09	**Holiday - No class**	
Week 3: **23/09** – 29/09	**MOS large and small-signal models, regimes of operations** + Exercise2	Ch 2.1-2.4
Week 4: **30/09** – 06/10	**MOS parasitic effects, layout basic, single-stage amplifiers** + Exercise3	Ch 2.1-2.4, Ch 3.1
Week 5: **07/10** – 13/10	**Single-stage amplifiers** + Exercise4	Ch 3.1-3.7
Week 6: **14/10** – 20/10	**Single-stage amplifiers** + Exercise5	Ch 3.1-3.7
Week 7: **21/10** – 27/10	**Holiday – No class**	
Week 8**: 28/10** – 03/11	**Single-stage amplifiers + Cascode** + Exercise6 + **Homework1**	Ch 4.1-4.4
Week 9: **04/11** – 10/11	**Differential amplifiers** + Exercise7	Ch 4.1-4.4
Week 10: **11/11** – 17/11	**TP1** Practical exercise session on Cadence	Tutorial on Moodle
Week 11: **18/11** – 24/11	**TP2** Practical exercise session on Cadence	Tutorial on Moodle
Week 12: **25/11** – 01/12	**TP3** Practical exercise session on Cadence + **Homework2**	Tutorial on Moodle
Week 13: **02/12** – 08/12	**TP4** Practical exercise session on Cadence	Tutorial on Moodle
Week 14: **09/12** – 15/12	**Differential amplifiers, current mirrors** + Exercise8	Ch 4.1-4.4, Ch 5.1-5.3
Week 15: **16/12** – 22/12	**Current mirrors** + Exercise9	Ch 5.1-5.3

- **TPs will be held in BC07-08**

Assessment:

- **Written final exam**: 70% of the final grade
- **Homework (2 in total)**: 30% of the final grade