

EE-320 Exercise 9

1. Consider the following circuit, where $(W/L)_P$ represents the size of PMOS transistors, and $(W/L)_{1,2}$ is the size of M_1 and M_2 . The input CM level applied to the gates of M_1 and M_2 is equal to V_{CM} . Assuming $\lambda = 0$, find the equation for V_P and the drain voltages of the PMOS diode-connected transistors (V_F and V_Y).

